

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,158,356 B2
APPLICATION NO. : 10/792078
DATED : January 2, 2007
INVENTOR(S) : Yong Shen et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 3, LINE 20

Please insert --20-- between "layer" and "varies"

COLUMN 3, LINE 42

Please insert --ABS-- after "Numeral 60"

COLUMN 3, LINE 56

Please insert --MR-- after "the"

COLUMN 4, LINE 40

Replace " $\frac{R_{SAL} * L_{SAL}}{R_{MR} * W_{MR} + R_{SAL} * L_{SAL}}$ " with -- $\frac{R_{SAL}}{(R_{MR} + R_{SAL})}$: voltage shunting factor,--.

COLUMN 4, LINE 65

Replace " $\frac{R_{SAL}}{(R_{MR} + R_{SAL})}$: voltage shunting factor," with -- $\frac{R_{SAL} * L_{SAL}}{R_{MR} * W_{MR} + R_{SAL} * L_{SAL}}$ --

Signed and Sealed this

Twenty-ninth Day of July, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office